

**Amendments to the Claims:**

**This listing of claims will replace all prior versions, and listings, of claims in the application:**

**Listing of Claims:**

1. (Currently amended) An integrated circuit comprising a network, a first electronic module and a plurality of second electronic modules arranged to communicate to each other via the network, the network is arranged to establish transactions between a first electronic module and at least two second electronic modules, the circuit comprises:

a network interface device means for receiving replicating a single request including at least one multicast address reference from the first electronic module, replicating the single request into at least two replicated requests, and for sending each of the at least two replicated requests to the respective second electronic modules; and, said means for replicating comprises

an address space and a facility for mapping the at least one multicast address reference onto at least two further a plurality of address space addresses in a range of addresses of the address space, each range is associated with addresses of one of the second electronic modules, the range of addresses comprises a first subset range of addresses and a second subset range of addresses, the number of addresses in each of the first and second subset ranges of addresses is greater than a single address,

wherein a first one of each of the at least two replicated requests is sent by the network interface to the second electronic modules is using its associated with the first

~~subset range of addresses such that any request sent to any of the first subset range of address space is sent to the first one of the second electronic modules and a second one of the second electronic modules is associated with the second subset range of addresses such that any request sent to any of the second subset range of address is sent to the second one of the second electronic modules.~~

2. (Canceled)

3. (Currently amended) The integrated circuit as claimed in claim 1, wherein the ~~means for replicating further comprises a facility for mapping~~ prevents mapping the at least one first multicast address reference onto the same address in the address space as the at least one second multicast address, ~~provided that the second multicast address is not mapped onto the first multicast address.~~

4. (Currently amended) The integrated circuit as claimed in claim 1, wherein the ~~means for replicating further comprises a facility for mapping~~ maps a range of multicast addresses reference onto ~~at least the first and second subset ranges of addresses in the address~~ space.

5. (Previously presented) The integrated circuit as claimed in claim 1, wherein the single request comprises a connection identifier for identifying a multicast connection, wherein the

multicast connection includes at least one of guaranteed throughput, latency and jitter, ordered delivery, and flow control.

6. (Currently amended) The integrated circuit as claimed in claim 1, wherein ~~means for replicating comprises a~~ the network interface device comprises a circuit for performing the replication of the single request into the replicated requests, and ~~wherein the network interface circuit sends~~ sending the replicated requests to the second electronic modules.

7. (Currently amended) A method for shifting a burden of sending requests to a network interface device in an integrated circuit comprising a network, a first electronic module and a plurality of second electronic modules, which communicate with each other via the network, the method further establishes transactions between the first electronic module and at least two second electronic modules, the method comprising acts of:

on the network interface device,

receiving ~~replicating~~ a single request including at least one multicast address reference from the first electronic module,

replicating the single request into at least two replicated requests, and

sending each of the replicated requests to the respective second electronic modules; and

on an address space and a facility for mapping the at least one multicast address reference onto at least two further a plurality of the address space addresses in a range of addresses of the address space, each range is associated with addresses of one wherein

~~the range of addresses comprises a first subset range of addresses and a second subset range of addresses of the second electronic modules, the number of addresses in each of the first and second subset ranges of addresses is greater than a single address,~~

~~wherein a first one of each of the replicated requests is sent to the second electronic modules is using its associated with the first subset range of addresses such that any request sent to any of the first subset range of address is sent to the first one of the second electronic modules and a second one of the second electronic modules is associated with the second subset range of addresses such that any request sent to any of the second subset range of address is sent to the second one of the second electronic modules~~space.

8. (Currently amended) The integrated circuit as claimed in claim 1, wherein the means for replicating ~~further comprises a facility for mapping~~ prevents mapping the at least one first multicast address reference onto the same address in the address space as two or more multicast addresses associated with ~~a single one of the~~ same second electronic modules.

9. (Currently amended) The integrated circuit as claimed in claim 1, wherein the facility for mapping ~~the at least one multicast address onto at least two further addresses in the range of addresses is arranged for further mapping the at least one multicast address onto at least one range of addresses that in turn is mapped to the plurality of second electronic modules.~~